

Abstract of the Disclosure

An equalizer circuit includes a carrier sensor, first and second equalizer units, carrier sense controller, and reception signal switching unit. The carrier sensor senses the start of a reception signal on 5 the basis of a signal representing the reception level of the reception signal. The first and second equalizer units equalize the reception signal. The carrier sense controller alternately enables the first and second equalizer units every frame reception in accordance with 10 an output from the carrier sensor. The reception signal switching unit alternately switches between outputs from the first and second equalizer units every frame reception and outputs the selected output as demodulation data.

DECODED
TRANSMITTED
DATA
CODED
TRANSMITTED
DATA